

BRAEMAR
COMPUTER
DEVICES, INC.

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April 22, 1980

Don Berman
Techexport Inc.
139 First Street
Cambridge, Mass. 02141

Dear Don:

In reference to your letter of 3/31 regarding the inquiry from South Africa.

I have attached our drawing 300-0256 which shows a hardware decoding approach for use at 2.4 or 4.8K baud. This is a modified version of the board we normally use in our CS400A. The CS400A operates at 8K baud and I have also enclosed a schematic for it.

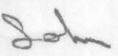
SK10307 is another hardware approach used with the mini. It is designed to operate at 2.4K baud only. To date we have not expanded its range to higher rates; however, I believe it will probably be a matter of capacitor changes, if your customer wishes to attempt it.

I have also enclosed the instruction manual for the CS400A, a print (300-0259) for the mother board and SK10080. This will provide your customer with timing information necessary to implement the encode/decode board (300-0255 or 300-0256).

If he requires any further assistance, please contact us.

Yours truly,

BRAEMAR COMPUTER DIVICES, INC.


John C. Rooks
Vice President
Engineering & Technical Sales

Att:300-0255 P2
300-0256 P4
300-0259 P2
SK10307
CS400 Manual
SK10080

P.S. If he chooses to use the SK10307 decoding approach, we have found that the 2N5457 JFETS made by Intersil permit the circuit to accept a wider speed range. I suggest that he test his circuit by connecting an oscillator to the input and observing the window opening point (approximately 67%) over a range of frequencies.